

**AMENDMENTS TO THE SPECIFICATION**

**Please amend paragraph [0036] of the specification as follows:**

If desired, pixel cell 200 can also include a graded buffer layer between a bottom layer 22-z of photodiode 220 heterostructure and substrate 201. Where forming a bottom layer 22-z of the photodiode 220 heterostructure in contact with substrate ~~[[220]]~~ 201 would result in lattice mismatch, graded buffer layer 206 may be used to reduce strain from lattice mismatch as is known in the art.

**AMENDMENTS TO THE DRAWINGS**

Please replace the sheet containing Figure 5C with the attached replacement sheet. The replacement sheet amends Figure 5C by adding reference numerals 230a (first insulating layer), 230b (conductive layer), and 230c (second insulating layer).